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**1 1.8 V RF AGC and mixer implemented with a novel four-terminal HB (FHBT)**  
*Sining Zhou; Pingxi Ma; Liyang Zhang; Zampardi, P.; Jiang Li; Chang, Y.S.; Chang, M.F.;*  
Radio Frequency Integrated Circuits (RFIC) Symposium, 2001. Digest of Papers, 2001 IEEE , 20-22 May 2001  
Pages:241 - 244

[\[Abstract\]](#) [\[PDF Full-Text \(228 KB\)\]](#) **IEEE CNF**

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#	Document ID	Issue Date	Pages	Title	Current ORIS	Current XRef	Retrieval Classif.	Inventor	SL	CDS	EP	GB
1	US 20040106380 A1	20040603	26	Direct-conversion transceiver enabling digital calibration	455/73	455/78; 455/83		Vassilou, Jason et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	US 20040082307 A1	20040429	10	Mixer circuit	455/318	455/313		Hack, Sabine	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	US 20040056726 A1	20040325	26	Integrated, digitally-controlled crystal oscillator	331/158			Dent, Paul W. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	US 20040033787 A1	20040219	30	Method and apparatus for signal power loss reduction in RF communication systems	455/78	455/82; 455/83		Weber, David et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	US 20040027187 A1	20040212	7	Mixer circuit	327/355			Takahashi, Yoshinori et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	US 20040012433 A1	20040122	21	Adaptive noise filtering and equalization for optimal high speed multilevel signal decoding	327/350	375/345		Kim, Andrew Joo et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	US 20040005869 A1	20040108	17	Wireless communications transceiver, transmitter using a harmonic reflection mixer and an I/F output offset phase-locked loop, a two-step up-converted	455/102	455/103		See, Puay-Hoe Andrew et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	US 20040004674 A1	20040108	19	Dual mode tuner for co-existing digital and analog television signals	348/731	348/705		Bilesen, Vince	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	US 20040002317 A1	20040101	8	Filter switching system and method	455/266	455/150.1; 455/191.3		Busking, Erik	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	US 20030235311 A1	20031225	14	Audio testing system and method	361/58	361/56		Grancic, Alexander et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	US 20030235145 A1	20031225	27	Crosstalk compensation engine for reducing signal crosstalk effects within a data signal	370/201			Shanbhag, Abhilash G. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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